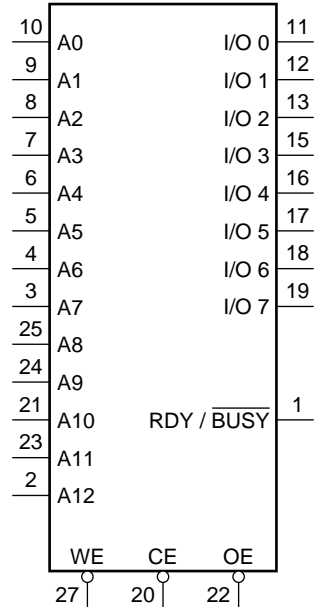
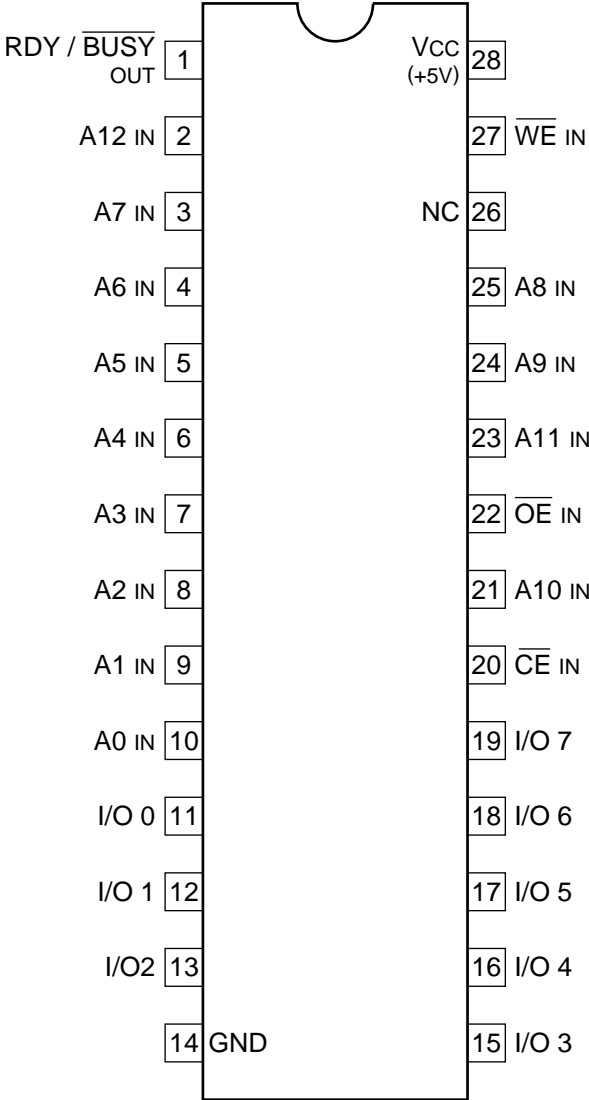


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C-MOS 64K (8K X 8) ELECTRICALLY ERASABLE PROM  
-TOP VIEW-



- A0 - A12 ; ADDRESS INPUTS
- I/O0 - I/O7 ; DATA INPUTS / OUTPUTS
- $\overline{\text{CE}}$  ; CHIP INABLE INPUT
- $\overline{\text{OE}}$  ; OUTPUT ENABLE, INPUT
- WE ; WRITE ENABLE INPUT
- RDY /  $\overline{\text{BUSY}}$  ; READY /  $\overline{\text{BUSY}}$  OUTPUT

